

Electronic Acknowledgement Receipt

EFS ID:	10826017
Application Number:	10590644
International Application Number:	
Confirmation Number:	1496
Title of Invention:	Phase-locked loop circuit
First Named Inventor/Applicant Name:	Syuji Kimura
Customer Number:	33356
Filer:	John Edward Gunther
Filer Authorized By:	
Attorney Docket Number:	SI08-P06576US
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Application Type:	U.S. National Stage under 35 USC 371

Payment information:

Submitted with Payment	yes
Payment Type	Credit Card
Payment was successfully received in RAM	\$ 1810
RAM confirmation Number	4748
Deposit Account	
Authorized User	

File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
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1	Issue Fee Payment (PTO-85B)	P06576-Issue-Fee-Paid-8-26-2011.pdf	199194 3f8357ae77f149ae141ef1f488a1d7b010542d30	no	1
Warnings:					
Information:					
2	Fee Worksheet (SB06)	fee-info.pdf	31882 ca9d2560eb9b6bb468c32d2d0fdc9cf029e8c6f8	no	2
Warnings:					
Information:					
Total Files Size (in bytes):			231076		
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